

WEST Search History

[Hide Items](#)[Restore](#)[Clear](#)[Cancel](#)

DATE: Friday, October 07, 2005

Hide?	Set Name	Query	Hit Count
		<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L7	l2 and L6	43
<input type="checkbox"/>	L6	700/83,259,264.ccls.	1955
		<i>DB=PGPB; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L5	717/104,123.ccls.	202
		<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L4	L3 and l2	70
<input type="checkbox"/>	L3	717/101-109,115,123.ccls.	1843
<input type="checkbox"/>	L2	L1 and ((graphic\$6 or visual\$7) with (diagram\$2 or process\$2 or step\$2))	3875
<input type="checkbox"/>	L1	(block\$2 with diagram\$2) and (process\$2 with specification\$2)	17849

END OF SEARCH HISTORY


[Subscribe](#) (Full Service) [Register](#) (Limited Service, Free) [Login](#)

 Search: ☒ The ACM Digital Library ☐ The Guide



THE ACM DIGITAL LIBRARY


[Feedback](#) [Report a problem](#) [Satisfaction survey](#)

 Terms used **block diagram process specification**

Found 3 of 164,603

Sort results by


[Save results to a Binder](#)
[Try an Advanced Search](#)

Display results


[Search Tips](#)
[Try this search in The ACM Guide](#)
☐ Open results in a new window

Results 1 - 3 of 3

 Relevance scale ☐ ☐ ☐ ☐ ☐

1 [The server network generator \(SNG\): a CASE tool for distributed cooperative processing](#)



L. E. Zeidner

 July 1991 **ACM SIGAPL APL Quote Quad , Proceedings of the international conference on APL '91**, Volume 21 Issue 4

 Full text available: [pdf\(1.28 MB\)](#)

 Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

The Server Network Generator (SNG) is a CASE tool that employs a problem solver's ability to represent an application as an ordinary block diagram, a graphical specification of its macroscopic structure. This functional decomposition provides a natural mechanism for subdividing the application into processing tasks that can be distributed across a computing network. Each "server" is a software process that assumes the role of one block in the diagram, performing one processing task, employing in ...

2 [State table analysis of programs in an algo-like language](#)



David L. Parnas

 January 1966 **Proceedings of the 1966 21st national conference**

 Full text available: [pdf\(713.94 KB\)](#)

 Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

a. The source language—SFD-ALGOL SFD-ALGOL 1.2 is an ALGOL-like language intended to describe the functions of synchronous systems. The function of a system is the response of its output to any input sequence. This function can be described by an algorithm. The algorithm need not be the one actually used by the system; it need only be equivalent. ALGOL is suitable for such a purpose except that it cannot express timing, specify input re ...

3 [Session W-24: The qualitative problem solving system PHYSYS](#)



Ali Manafi Shemirani, John C. Thompson

 February 1989 **Proceedings of the 17th conference on ACM Annual Computer Science Conference**

 Additional Information: [full citation](#), [abstract](#)

Any expert system designer should consider answering questions such as "What knowledge must be available to the system so that it reaches the solution of a given problem," "How should that knowledge be represented," and "What Programming Language is most suited for implementation of such a system." It has been shown by research on various types of rule-based systems that representations of prototypical situations confronted in a domain are one kind of knowledge crucial to the performance comp ...

Results 1 - 3 of 3

The ACM Portal is published by the Association for Computing Machinery. Copyright © 2005 ACM, Inc.

[Terms of Usage](#) [Privacy Policy](#) [Code of Ethics](#) [Contact Us](#)

Useful downloads:  [Adobe Acrobat](#)  [QuickTime](#)  [Windows Media Player](#)  [Real Player](#)

[Home](#) | [Login](#) | [Logout](#) | [Access Information](#) | [Alerts](#) |

Welcome United States Patent and Trademark Office

Search Results[BROWSE](#)[SEARCH](#)[IEEE XPLORE GUIDE](#)

Results for "((block diagram <in>metadata) <and> (process specification<in>metadata))"

e-mail

Your search matched **0** documents.A maximum of **100** results are displayed, **25** to a page, sorted by **Relevance** in **Descending** order.

» Search Options

[View Session History](#)[New Search](#)

Modify Search

☐ Check to search only within this results setDisplay Format: ☒ Citation ☐ Citation & Abstract

» Key

IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

No results were found.

Please edit your search criteria and try again. Refer to the Help pages if you need assistance search.

[Help](#) [Contact Us](#) [Privacy & :](#)

© Copyright 2005 IEEE --

Indexed by




[Home](#) | [Login](#) | [Logout](#) | [Access Information](#) | [Alerts](#) |

Welcome United States Patent and Trademark Office

Search Results

[BROWSE](#)[SEARCH](#)[IEEE XPLORE GUIDE](#)

Results for "((block diagram <in>metadata) <and> (process<in>metadata))<and> (spec..."

[e-mail](#)

Your search matched 13 of 1242336 documents.

A maximum of 100 results are displayed, 25 to a page, sorted by **Relevance** in **Descending** order.

» Search Options

[View Session History](#)[New Search](#)

Modify Search

((block diagram <in>metadata) <and> (process<in>metadata))<and> (specificatio [»](#)☐ Check to search only within this results set

» Key

Display Format: ☒ Citation ☐ Citation & Abstract

IEEE JNL IEEE Journal or Magazine

Select Article Information

IEEE JNL IEEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEEE CNF IEEE Conference Proceeding

IEEE STD IEEE Standard

- ☐ 1. **Shared memory implementations of synchronous dataflow specifications**
Murthy, P.K.; Bhattacharyya, S.S.;
Design, Automation and Test in Europe Conference and Exhibition 2000. Proc
27-30 March 2000 Page(s):404 - 410
Digital Object Identifier 10.1109/DATE.2000.840303
[AbstractPlus](#) | Full Text: [PDF](#)(52 KB) IEEE CNF
- ☐ 2. **A robust image processing language in the context of image algebra**
Dougherty, E.R.; Sehdev, P.;
Computer Vision and Pattern Recognition, 1988. Proceedings CVPR '88., Conf
Conference on
5-9 June 1988 Page(s):748 - 753
Digital Object Identifier 10.1109/CVPR.1988.196318
[AbstractPlus](#) | Full Text: [PDF](#)(348 KB) IEEE CNF
- ☐ 3. **Software synthesis and code generation for signal processing systems**
Bhattacharyya, S.S.; Leupers, R.; Marwedel, P.;
Circuits and Systems II: Analog and Digital Signal Processing, IEEE Transacti
Circuits and Systems II: Express Briefs, IEEE Transactions on]
Volume 47, Issue 9, Sept. 2000 Page(s):849 - 875
Digital Object Identifier 10.1109/82.868454
[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(484 KB) IEEE JNL
- ☐ 4. **Shared buffer implementations of signal processing systems using lifetin techniques**
Murthy, P.K.; Bhattacharyya, S.S.;
Computer-Aided Design of Integrated Circuits and Systems, IEEE Transacti
Volume 20, Issue 2, Feb 2001 Page(s):177 - 198
Digital Object Identifier 10.1109/43.908427
[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(464 KB) IEEE JNL
- ☐ 5. **A synchronous model of IEC 61131 PLC languages In SIGNAL**
Jimenez-Fraustro, F.; Rutten, E.;
Real-Time Systems, 13th Euromicro Conference on, 2001.
13-15 June 2001 Page(s):135 - 142
Digital Object Identifier 10.1109/EMRTS.2001.934016
[AbstractPlus](#) | Full Text: [PDF](#)(672 KB) IEEE CNF

- ☐ **6. Systematic consolidation of input and output buffers in synchronous dat specifications**
Murthy, P.K.; Bhattacharyya, S.S.;
Signal Processing Systems, 2000. SiPS 2000. 2000 IEEE Workshop on
11-13 Oct. 2000 Page(s):673 - 682
Digital Object Identifier 10.1109/SIPS.2000.886765
[AbstractPlus](#) | Full Text: [PDF](#)(512 KB) IEEE CNF

- ☐ **7. Software design methodology for power electronics applications**
Carmeli, S.; Cosatto, E.; Monti, A.; Penno, C.;
Computers in Power Electronics, 1998. 6th Workshop on
19-22 July 1998 Page(s):XVII - XXXIII
Digital Object Identifier 10.1109/CIPE.1998.779650
[AbstractPlus](#) | Full Text: [PDF](#)(1572 KB) IEEE CNF

- ☐ **8. Conceptual design of an ultra high-resolution MM-wave synthetic apertur**
Griffiths, H.D.;
Radar Conference, 1996., Proceedings of the 1996 IEEE National
13-16 May 1996 Page(s):255 - 260
Digital Object Identifier 10.1109/NRC.1996.510690
[AbstractPlus](#) | Full Text: [PDF](#)(292 KB) IEEE CNF

- ☐ **9. CADiSP-a graphical compiler for the programming of DSP in a completely**
Knoll, A.; Nieberle, R.;
Acoustics, Speech, and Signal Processing, 1990. ICASSP-90., 1990 Internatio
on
3-6 April 1990 Page(s):1077 - 1080 vol.2
Digital Object Identifier 10.1109/ICASSP.1990.116111
[AbstractPlus](#) | Full Text: [PDF](#)(344 KB) IEEE CNF

- ☐ **10. Robot controller specification using SART approach**
Urbain, L.; Tondou, B.;
Control Applications, 1994., Proceedings of the Third IEEE Conference on
24-26 Aug. 1994 Page(s):303 - 308 vol.1
Digital Object Identifier 10.1109/CCA.1994.381450
[AbstractPlus](#) | Full Text: [PDF](#)(408 KB) IEEE CNF

- ☐ **11. A psychological basis for the design of computer systems which suppor activity**
Jagodzinski, A.P.; Ball, L.; Evans, J.St.B.; Dennis, I.; Baker, K.D.; Culverhouse
P.D.; Scothern, D.; Venner, G.;
HCI: Issues for the Factory, IEE Colloquium on
21 Feb 1991 Page(s):1/1 - 1/4
[AbstractPlus](#) | Full Text: [PDF](#)(176 KB) IEE CNF

- ☐ **12. Using VHDL for high-level, mixed-mode system simulation**
Srivastava, M.B.; Brodersen, R.W.;
Design & Test of Computers, IEEE
Volume 9, Issue 3, Sept. 1992 Page(s):31 - 40
Digital Object Identifier 10.1109/54.156156
[AbstractPlus](#) | Full Text: [PDF](#)(960 KB) IEEE JNL

- ☐ **13. Automatic generation of availability models in RAScad**
Dong Tang; Ji Zhu; Andrada, R.;
Dependable Systems and Networks, 2002. Proceedings. International Confere
23-26 June 2002 Page(s):488 - 492
Digital Object Identifier 10.1109/DSN.2002.1028935
[AbstractPlus](#) | Full Text: [PDF](#)(287 KB) IEEE CNF



Indexed by
Inspection

[Help](#) [Contact Us](#) [Privacy & ;](#)

© Copyright 2005 IEEE -

Day : Friday
Date: 10/7/2005

Time: 14:26:59

 **PALM INTRANET**

Inventor Information for 09/940189

Inventor Name	City	State/Country
VAZQUEZ, NICOLAS	AUSTIN	TEXAS
SCHULTZ, KEVIN L.	GEORGETOWN	TEXAS

[Appln Info](#)[Contents](#)[Petition Info](#)[Atty/Agent Info](#)[Continuity Data](#)[Foreign Data](#)

Search Another: Application# **or Patent#**

PCT / **/** **or PG PUBS #**

Attorney Docket #

Bar Code #

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)